

**REMARKS**

Claims 1-36 are pending in this application, of which claims 17-28 have been withdrawn from consideration. Claim 1 has been amended. Claims 29-36 are newly-added.

The Examiner has objected to claim 1 on line 6 for an informality.

Accordingly, claim 1 has been amended to correct correcting this informality, but "a" has been replaced with "N" instead of " $\alpha$ ", as recommended by the Examiner.

Claims 1-16 stand under 35 USC §112, second paragraph, as indefinite.

In this regard, Applicant respectfully submits the following:

First, for example, claim 1 corresponds to Fig. 5, claim 11 corresponds to Fig. 7, and claim 14 corresponds to Fig. 8. Second, in order to more clearly identify the claimed elements, claims 1, 11 and 14 have been rewritten below to include the references described in each of the corresponding drawings .

1. A timing controller comprising:

a first circuit (1) having a first delay time (IB-1); a second circuit (2) having a second delay time (IB-2); and a time difference expander (3) for expanding a time difference ( $\tau$ ) between a changeover point of a first signal (A) and a changeover point of a second signal (B) N times (N being a value greater than one) , to provide an output signal having a given time difference with respect to a control signal (CLK) , the first signal being passed through said first circuit and said second circuit, and the second signal being passed through said first circuit. {Fig. 5)

11. A timing controller comprising an internal circuit (1, 21, 22, 23, ...), and a time difference expander (3) for expanding a time difference between a changeover point of a first signal (A) and a changeover point of a second signal (B) N times (N being an integer equal to or greater than two), to provide a phase-controlled output signal, the first signal being passed through said internal circuit and produced by a cycle of a control signal (CLK) , and the second signal being passed through a part of said internal circuit and produced by the next cycle of the control signal. (Fig. 7)

14. A timing controller comprising a first internal circuit (1, 21, 22, 23, ...), a time difference expander (3) for expanding a time difference (x) between a changeover point of a first signal (A) and a changeover point of a second signal (B) N times (N being an integer equal to or greater than two), to provide a phase-controlled output signal, and a second internal circuit (4, 5) for producing a phase-controlled signal according to an output of said time difference expander, the first signal being passed through said first internal circuit and produced by a cycle of a control signal, the second signal being passed through a part (21, 22) of said first internal circuit and produced by the next, cycle of the control signal, a delay time of said second internal circuit being substantially equal to a delay time of a specific part of said first internal circuit. (Fig. 8)

It is respectfully submitted that the description above properly identifies all claimed elements and where they appear in the drawings, and the 35 USC §112, second paragraph, rejection should be withdrawn.

U.S. Patent Application Serial No. **10/708,145**  
Response to Office Action dated September 8, 2004

In view of the aforementioned amendments and accompanying remarks, claims 1-16 and 29-36, as amended, are in condition for allowance, which action, at an early date, is requested.

If, for any reason, it is felt that this application is not now in condition for allowance, the Examiner is requested to contact Applicant's undersigned attorney at the telephone number indicated below to arrange for an interview to expedite the disposition of this case.

In the event that this paper is not timely filed, Applicant respectfully petitions for an appropriate extension of time. Please charge any fees for such an extension of time and any other fees which may be due with respect to this paper, to Deposit Account No. 01-2340.

Respectfully submitted,

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Enclosures: Amendment Transmittal

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